

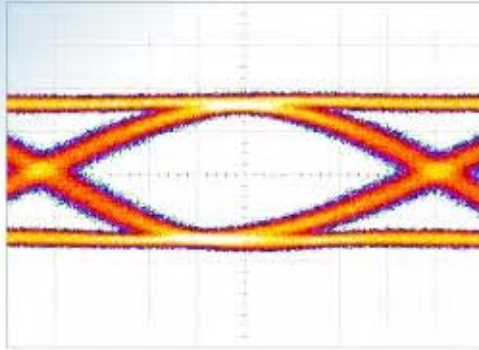


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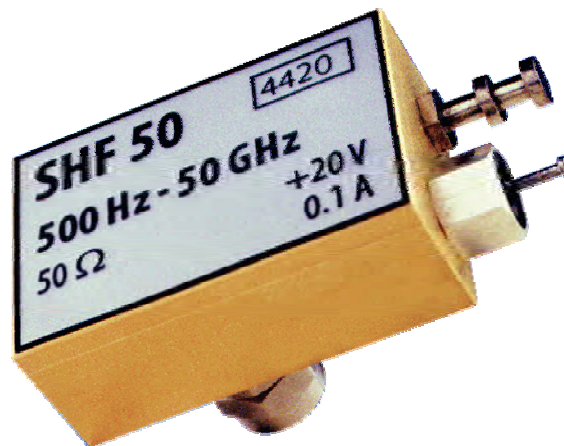
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Datasheet

SHF 50

Bias Load



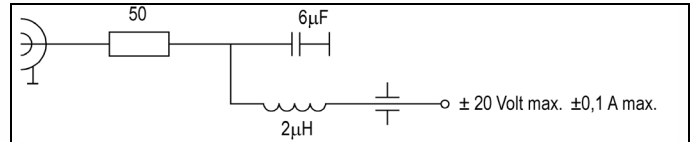


Specifications – SHF 50

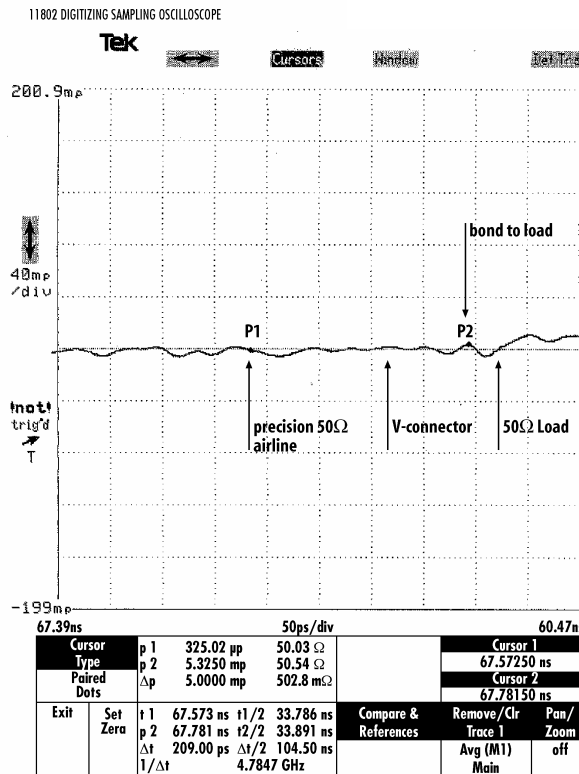
Parameter	Symbol	Unit	Min	Max	Conditions
High frequency 3dB point	f_{HIGH}	GHz	50		
Low frequency 3dB point	f_{LOW}	Hz		500	
Input return loss	S_{11}	dB		30 20 10	<5 GHz <20 GHz <40 GHz
Pulse power	P_{max}	dBm		36	<5% duty cycle
Supply current	I_{max}	A		0.1	maximum with RF power below 0dBm CW; DC + RF power should not exceed 24dBm!
Supply voltage	V_{max}	V		20	
RF connector					V male*
Dimensions LxWxH		mm			20x30x12 plus connectors

* K male connector also available on request. Please note that this will reduce f_{HIGH} to ~45GHz

The SHF 50 bias load allows a voltage to be added to a 50Ω with a high line while the line itself is a terminated precision load. It is especially suited for biasing optical modulators.

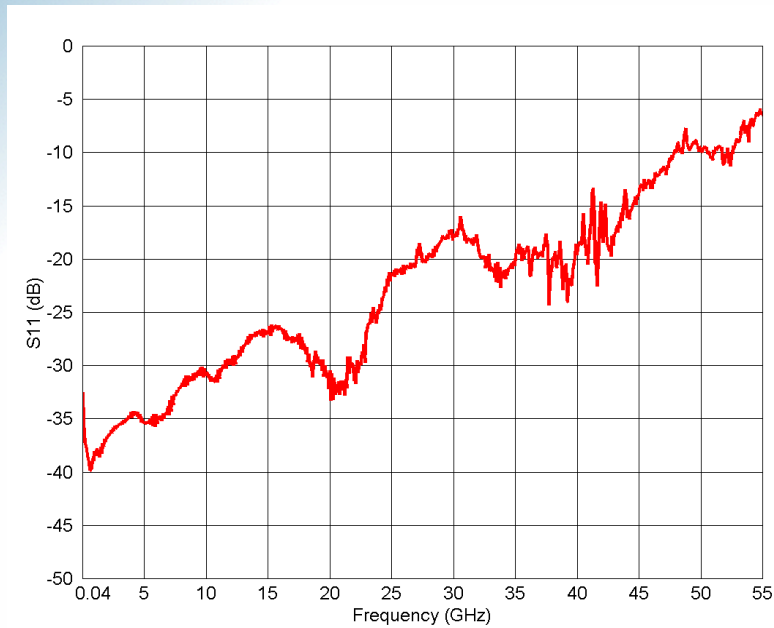


TDR measurement of SHF 50 Bias Load

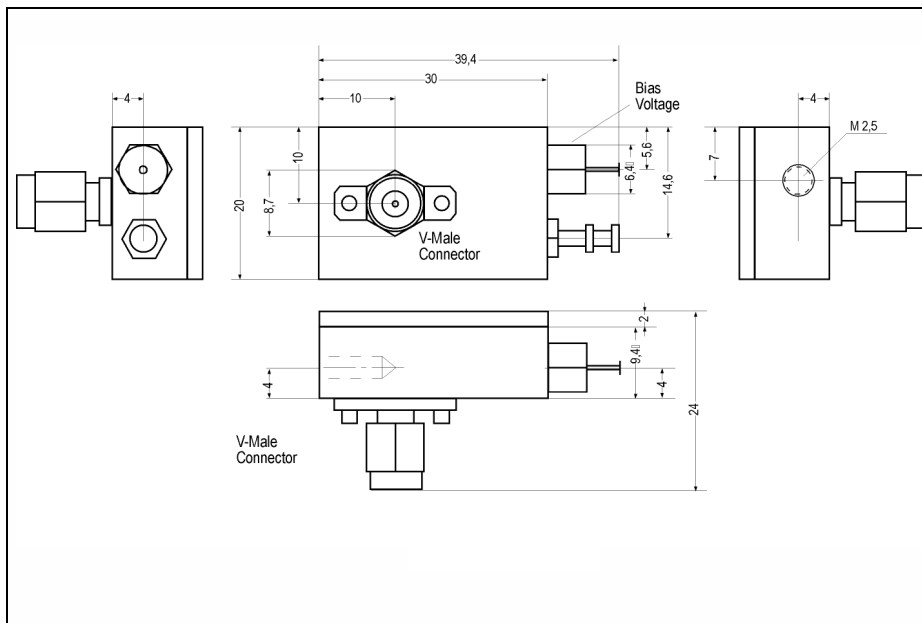




Input return loss



Outline drawing



ATTENTION

Check mating surface before connecting the bias load to a device under test.

Correct center conductor recess of the DUT is **essential** to avoid destroying the delicate mechanical layout inside the SHF 50.

For multiple connections, use a connector saver or an adaptor ahead of the SHF 50.

NEVER USE A WORN OUT CONNECTOR!