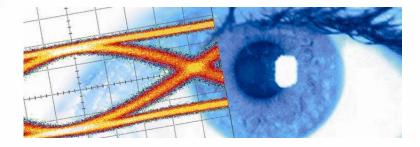


SHF Communication Technologies AG

Wilhelm-von-Siemens-Str. 23D • 12277 Berlin • Germany Phone +49 30 772051-0 • Fax ++49 30 7531078 E-Mail: sales@shf.de • Web: http://www.shf.de



Datasheet SHF 11126 A 51 to 65 Gbps Clock Recovery



For illustration only, actual product may vary





The SHF 11126 A clock recovery is designed to extract the clock from a serial NRZ input data stream. It covers a wide range of input data rates from 51 Gbps to 65 Gbps. The required internal reference clock is provided by an integrated synthesizer module.

The instrument is controlled remotely by Ethernet connection from a PC running the SHF BERT Control Center software (BCC) or the SHF Control Center (SCC). Its programming features allow automated measurements.

The compact size of the clock recovery allows placement very close to the DUT in the test setup.

Features

- Operating data rate range from 51 to 65 Gbps
- Operates also at sub-rates of 51 to 65 Gbps but extracted clock refers to full data rate
- Clock output frequency at half and optionally full rate of the data rate set in the GUI
- Remote operation via Ethernet connection from a host PC (SHF BERT Control Center)
- Compact size: 221.4 mm (W) x 50.8 mm (H) x 177 mm (D)

Applications

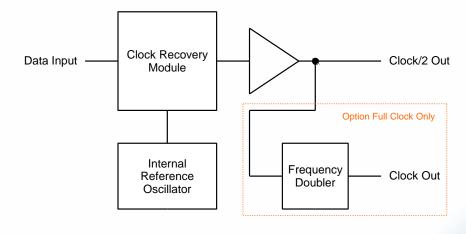
- R&D for characterization of chips, devices, transceiver modules and sub-components
- Characterization of high speed optical and electrical links
- Research, development, production tests, on-wafer testing
- OIF CEI 56G, InfiniBand®
- Proprietary interfaces (chip-to-chip, chip-to-module, backplanes, repeaters, and active optical cables)

Options

Option FC – Full Clock Out

Adds a frequency doubler providing a clock at the frequency of the nominal input data rate.

Block Diagram



SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 2/9



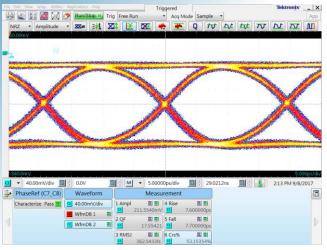
Specifications – SHF 11126 A

Parameter	Unit	Symbol	Min.	Тур.	Max.	Comment			
Data Input									
Input Bit Rate	Gbps		51		65	NRZ data format			
Input Level	mV	V _{eyeamp}	200		600				
Connector Type		Ω		50		2.92 mm (K) female			
Clock/2 Output									
Output Frequency	GHz	f _{out,clock}	25.5		32.5				
Output Level	mVpp	V _{out,clock}	550		750				
Output Jitter (RMS)	fs	J_{RMS}		300	450	with low jitter reference input signal			
Connector Type		Ω		50		2.92 mm (K) female			
Full Clock Output (Optional)									
Output Frequency	GHz	f _{out,clock}	51		65				
Output Level	mVpp	$V_{\text{out,clock}}$	500		1000				
Output Jitter (RMS)	fs	J_{RMS}			450	with low jitter reference input signal			
Connector Type		Ω		50		1.85 mm (V) female			
General									
Supply Voltage	Vee	V	11.5	12	12.5	+12V switching power supply is included			
Power Consumption	Ptot	W		7.2	8				
Height	Н	mm		50.8					
Width	W	mm		221.4					
Depth	D	mm		177					
Weight	m	g			1500				
Case Temperature	Tcase	°C	15		35				

SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 3/9









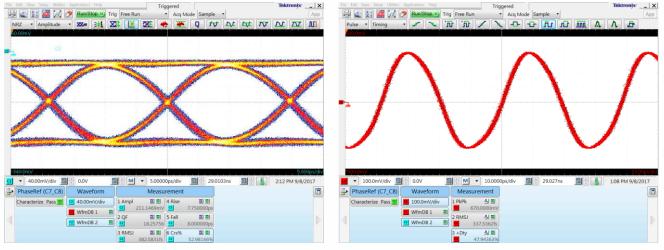
Data In @ 51 Gbps

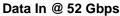
Clock/ Out @ 51 Gbps → 25.5 GHz

Trigge

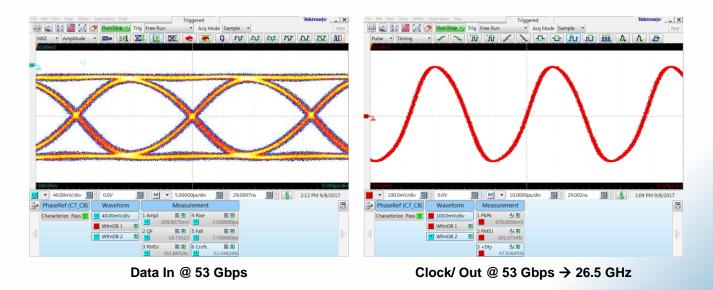
 Image: September 2010
 Image: September 2010

nix _ X



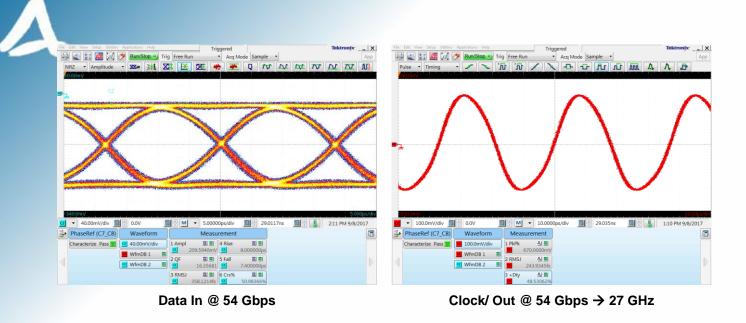


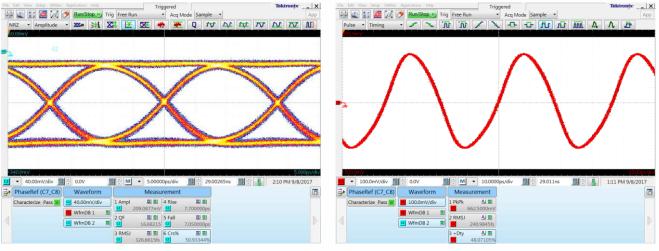
Clock/ Out @ 52 Gbps → 26 GHz



SHF reserves the right to change specifications and design without notice - SHF 11126 A - V001 - Sep 6, 2017 Page 4/9

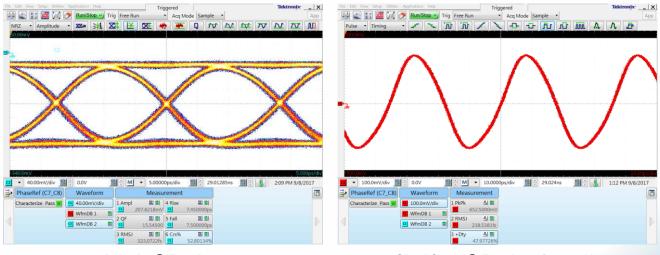






Data In @ 55 Gbps

Clock/ Out @ 55 Gbps → 27.5 GHz

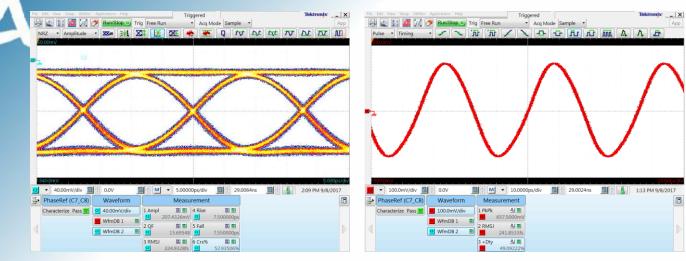




Clock/ Out @ 56 Gbps → 28 GHz

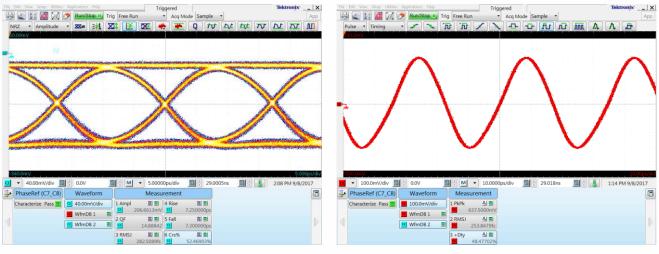
SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 5/9





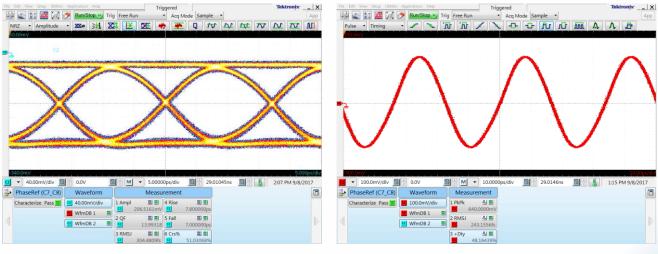
Data In @ 57 Gbps

Clock/ Out @ 57 Gbps → 28.5 GHz



Data In @ 58 Gbps

Clock/ Out @ 58 Gbps → 29 GHz

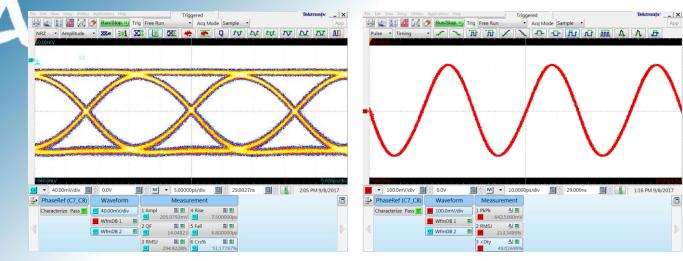




Clock/ Out @ 59 Gbps → 29.5 GHz

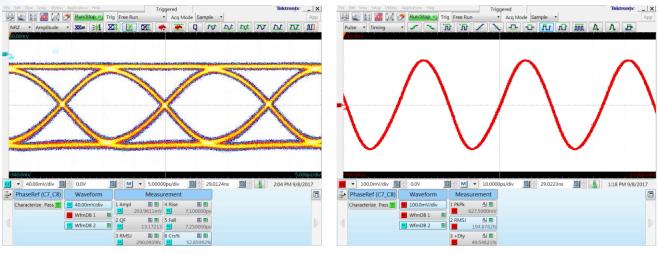
SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 6/9





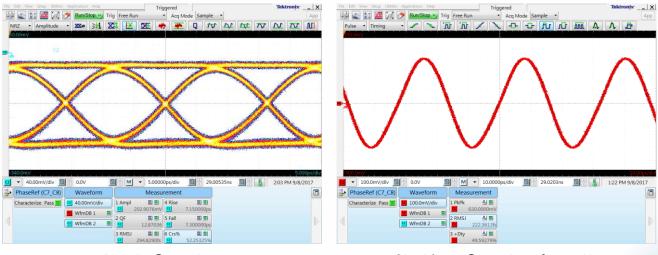
Data In @ 60 Gbps

Clock/ Out @ 60 Gbps → 30 GHz



Data In @ 61 Gbps

Clock/ Out @ 61 Gbps → 30.5 GHz

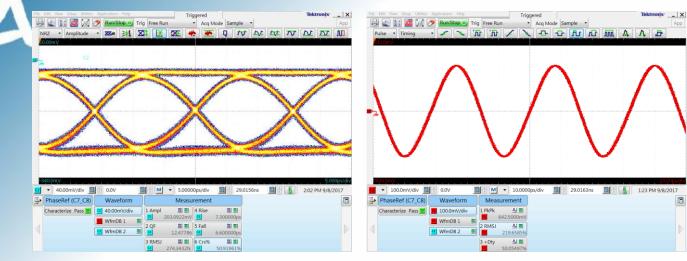


Data In @ 62 Gbps

Clock/ Out @ 62 Gbps → 31 GHz

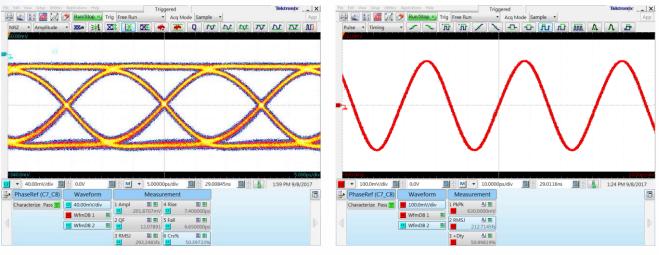
SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 7/9





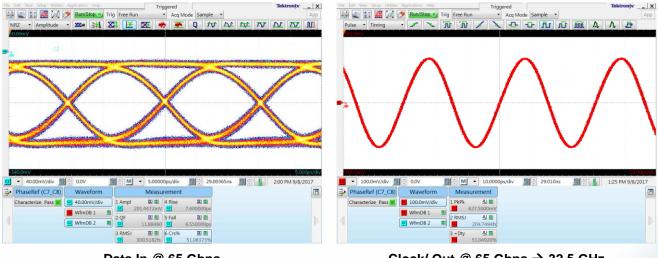
Data In @ 63 Gbps

Clock/ Out @ 63 Gbps → 31.5 GHz



Data In @ 64 Gbps

Clock/ Out @ 64 Gbps → 32 GHz



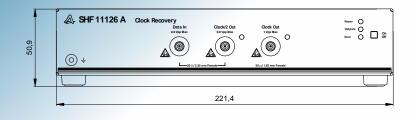
Data In @ 65 Gbps

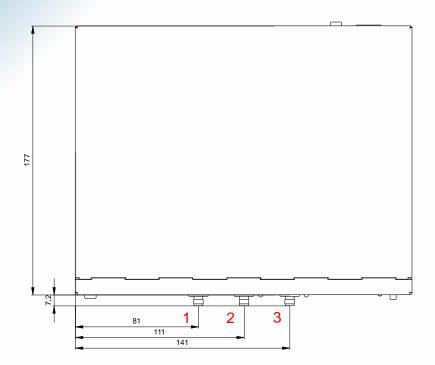
Clock/ Out @ 65 Gbps → 32.5 GHz

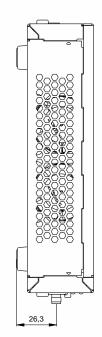
SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 8/9

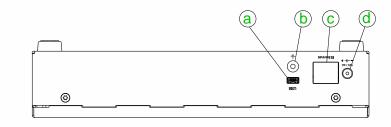












Pos.	Designation	Connector	Pos.	Designation
1	Data In	2.92mm (K) Female	а	USB
2	Clock/2 Out	2.92mm (K) Female	b	GND
3	Clock Out	1.85mm (V) Female	С	Ethernet
			d	Power Supply

all dimensions in mm

SHF reserves the right to change specifications and design without notice – SHF 11126 A - V001 – Sep 6, 2017 Page 9/9

